

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	282	(324/253).CCLS.	US-PGPU B; USPAT	OR	OFF	2004/09/03 16:51
S2	1	S1 and ((fluxgate or flux-gate or flux adj gate or magnetometer or MR) same converter same controller)	US-PGPU B; USPAT	OR	OFF	2004/09/08 14:37
S3	0	("1andlee.in.").PN.	US-PGPU B; USPAT	OR	OFF	2004/09/03 16:47
S4	1	("20040051566").PN.	US-PGPU B; USPAT	OR	OFF	2004/09/03 16:53
S5	496	327/110.ccls.	US-PGPU B; USPAT	OR	OFF	2004/09/03 16:53
S6	1	S5 and ((fluxgate or flux-gate or flux adj gate or magnetometer or MR) same controller same converter)	US-PGPU B; USPAT	OR	OFF	2004/09/07 08:03
S7	109	((fluxgate or flux-gate or flux adj gate or magnetometer or MR) same controller same converter)	US-PGPU B; USPAT	OR	OFF	2004/09/03 16:54
S8	1	((fluxgate or flux-gate or flux adj gate or magnetometer or MR) same controller same converter with feedback)	US-PGPU B; USPAT	OR	OFF	2004/09/03 16:55
S9	0	((fluxgate or flux-gate or flux adj gate or magnetometer) same controller same converter with feedback)	US-PGPU B; USPAT	OR	OFF	2004/09/03 16:55
S10	16	((fluxgate or flux-gate or flux adj gate or magnetometer) same controller same converter)	US-PGPU B; USPAT	OR	OFF	2004/09/07 08:36
S11	109	((fluxgate or flux-gate or flux adj gate or magnetometer or MR) same controller same converter)	US-PGPU B; USPAT	OR	OFF	2004/09/07 08:03
S12	16	((fluxgate or flux-gate or flux adj gate or magnetometer) same controller same converter)	US-PGPU B; USPAT	OR	OFF	2004/09/07 08:03

S13	27	(324/253-263.ccls. or 324/244.ccls.) and (and-gate or "and gate")	US-PGPU B; USPAT	OR	OFF	2004/09/07 09:13
S14	10	S13 and (fluxgate or flux-gate or flux adj gate or magnetometer)	US-PGPU B; USPAT	OR	OFF	2004/09/07 09:12
S15	10	S14 and @pd<"20020918"	US-PGPU B; USPAT	OR	OFF	2004/09/07 08:47
S16	1	"4053849".PN.	USPAT; USOCR	OR	OFF	2004/09/07 09:09
S17	1	"3944912".PN.	USPAT; USOCR	OR	OFF	2004/09/07 09:11
S18	2	"3218547".PN.	USPAT; USOCR	OR	OFF	2004/09/07 09:11
S19	2	"3218547".PN.	USPAT; USOCR	OR	OFF	2004/09/07 09:12
S20	1	"3944912".PN.	USPAT; USOCR	OR	OFF	2004/09/07 09:12
S21	1	"3649958".PN.	USPAT; USOCR	OR	OFF	2004/09/07 09:12
S22	28	(324/253-263.ccls. or 324/244.ccls.) and (and-gate or "and gate")	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 09:12
S23	10	S22 and (fluxgate or flux-gate or flux adj gate or magnetometer)	US-PGPU B; USPAT	OR	OFF	2004/09/07 09:13
S24	10	(324/253-263.ccls. or 324/244.ccls.) and (and-gate or "and gate") and (fluxgate or flux-gate or flux adj gate or magnetometer)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/09/07 09:13
S25	19	(and-gate or "and gate") same (fluxgate or flux-gate or flux adj gate or magnetometer)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/09/07 09:22

S26	1970	(and-gate or "and gate") same (generator) same (controller or microcontroller or micro-controller or microprocessor or micro-processor or processor)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/09/07 09:23
S27	1531	(and-gate or "and gate") same (generator) same (controller or microcontroller or micro-controller or microprocessor or micro-processor or processor)	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/08 09:16
S28	1387	S27 and @pd<"20020918"	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 09:24
S29	54	S28 and "324".clas.	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 09:31
S30	1	S29 and (magnetometer or fluxgate or flux-gate or "flux gate")	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 09:31
S31	1	S28 and (magnetometer or fluxgate or flux-gate or "flux gate")	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 09:31
S32	3541	(and-gate or "and gate") same (pulse generator) same (controller or microcontroller or micro-controller or microprocessor or micro-processor or processor)	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 09:37
S33	320	(and-gate or "and gate") same (pulse adj generator) same (controller or microcontroller or micro-controller or microprocessor or micro-processor or processor)	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 09:45
S34	296	S33 and @pd<"20020918"	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 09:38

S35	0	S34 and (fluxgate or flux-gate or "flux gate" or magnetometer)	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 09:38
S36	2	"3461367".PN.	USPAT; USOCR	OR	OFF	2004/09/07 09:42
S37	2	"3324369".PN.	USPAT; USOCR	OR	OFF	2004/09/07 09:42
S38	2	"3214663".PN.	USPAT; USOCR	OR	OFF	2004/09/07 09:42
S39	2	"3127548".PN.	USPAT; USOCR	OR	OFF	2004/09/07 09:43
S40	29	(and-gate or "and gate") near12 (pulse adj generator) near12 (controller or microcontroller or micro-controller or microprocessor or micro-processor or processor)	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 10:28
S41	26	S40 and @pd<"20020918"	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 09:48
S42	5	S41 and "324".clas.	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 09:48
S43	324	(and-gate or "and gate") near12 (pulse adj generator or counter) near12 (controller or microcontroller or micro-controller or microprocessor or micro-processor or processor)	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 09:56
S44	13	S43 and "324".clas.	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 09:56

S45	170	(and-gate or "and gate") same (pulse adj generator or counter) same (controller or microcontroller or micro-controller or microprocessor or micro-processor or processor or pulse adj controller or pulse-controller) same (high near3 signal) same (low near3 signal)	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 10:30
S46	0	(and-gate or "and gate") same (pulse adj generator or counter) same (controller or microcontroller or micro-controller or microprocessor or micro-processor or processor or pulse adj controller or pulse-controller) same (high near3 signal) same (low near3 signal) same (conversion with complete)	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 10:30
S47	0	(and-gate or "and gate") same (pulse adj generator or counter) same (controller or microcontroller or micro-controller or microprocessor or micro-processor or processor or pulse adj controller or pulse-controller) same (high near3 signal) same (low near3 signal) same (conversion) same complete	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 10:55
S48	156	S45 and @pd<"20020918"	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 10:30
S49	4	S48 and "324".clas.	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 10:30

S50	370	(and-gate or "and gate") same (pulse adj generator or counter) same (controller or microcontroller or micro-controller or microprocessor or micro-processor or processor or pulse adj controller or pulse-controller) same (high near3 (signal or pulse)) and low near3 (signal or pulse)	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 10:33
S51	17	S50 and "324".clas.	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 10:43
S52	1	("5333110").PN.	US-PGPU B; USPAT	OR	OFF	2004/09/07 10:43
S53	8	(pulse adj generator or counter) same (controller or microcontroller or micro-controller or microprocessor or micro-processor or processor or pulse adj controller or pulse-controller) same (high near3 signal) same (low near3 signal) same (conversion) same complete	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 10:57
S54	98	(pulse adj generator or counter) same (controller or microcontroller or micro-controller or microprocessor or micro-processor or processor or pulse adj controller or pulse-controller) same ((high or low) near3 signal) same (analog adj to adj digital or A/D or "a to d")	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 11:17
S55	79	S54 and @pd<"20020918"	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 11:02
S56	11	S55 and "324".clas.	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 11:00

S57	0	(controller or microcontroller or micro-controller or microprocessor or micro-processor or processor or pulse adj controller or pulse-controller) same ((high or low) near3 signal) same (analog adj to adj digital or A/D or "a to d") same coversion same complete	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 11:21
S58	0	(controller or microcontroller or micro-controller or microprocessor or micro-processor or processor or pulse adj controller or pulse-controller) same ((high or low) near3 signal) and (analog adj to adj digital or A/D or "a to d") same coversion same complete	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 11:22
S59	0	(controller or microcontroller or micro-controller or microprocessor or micro-processor or processor or pulse adj controller or pulse-controller) same ((high or low) near3 signal) and (analog near2 digital or A/D or "a to d") same coversion same complete	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 11:23
S60	0	(controller or microcontroller or micro-controller or microprocessor or micro-processor or processor or pulse adj controller or pulse-controller) same (analog near2 digital or A/D or "a to d") and ((high or low) near3 signal) same coversion same complete	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 11:24

S61	1	(controller or microcontroller or micro-controller or microprocessor or micro-processor or processor or pulse adj controller or pulse-controller) same (analog near2 digital or A/D or "a to d") and ((high or low) near3 signal) same coversion	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 11:25
S62	77785	(controller or microcontroller or micro-controller or microprocessor or micro-processor or processor or pulse adj controller or pulse-controller) same (analog near2 digital or A/D or "a to d")	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 11:26
S63	63217	(controller or microcontroller or micro-controller or microprocessor or micro-processor or processor or pulse adj controller or pulse-controller) same (analog adj digital or A/D or "a to d" or analog-digital or analog-to-digital)	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 11:34
S64	3557	S63 and "324".clas.	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 11:27
S65	124	S64 and (fluxgate or flux-gate or flux adj gate or magnetometer)	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 11:33
S66	4	S65 and ("and gate" or and-gate)	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 11:29
S67	15	S65 and control adj signal	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 11:33

S68	13	(controller or microcontroller or micro-controller or microprocessor or micro-processor or processor or pulse adj controller or pulse-controller) same (analog adj digital or A/D or "a to d" or analog-digital or analog-to-digital) same (fluxgate or flux-gate or flux adj gate or magnetometer) same control adj signal	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 11:39
S69	2	(controller or microcontroller or micro-controller or microprocessor or micro-processor or processor or pulse adj controller or pulse-controller) same (analog adj digital or A/D or "a to d" or analog-digital or analog-to-digital) same (fluxgate or flux-gate or flux adj gate or magnetometer) same (high or low) near2 (signal or pulse)	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 11:44
S70	1779	(controller or microcontroller or micro-controller or microprocessor or micro-processor or processor or pulse adj controller or pulse-controller) same (analog adj digital or A/D or "a to d" or analog-digital or analog-to-digital) same (high or low) near2 (signal or pulse)	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 11:45
S71	79	S70 and complete same conversion	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 11:45
S72	65	S71 and @pd<"20020918"	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 11:46

S73	15	S72 and ("324".clas. or "327".clas.)	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 12:53
S74	51	324/253.ccls. and (low with power)	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 12:54
S75	20	324/253.ccls. and (low with power with consumption)	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 13:44
S76	51	324/253.ccls. and (low with power)	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 14:12
S77	1	(324/253.ccls. or 327/110. ccls.) and (low with power same ("and gate" or and-gate))	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 14:14
S78	54	("324".clas. or "327clas.") and (low with power same ("and gate" or and-gate))	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 14:15
S79	0	("324".clas. or "327clas.") and (low with power same ("and gate" or and-gate)) and (fluxgate or flux-gate or flux adj gate or magnetometer)	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/07 14:27
S80	282	(324/253).CCLS.	US-PGPU B; USPAT	OR	OFF	2004/09/07 14:28
S81	7	S80 and ("and gate" or and-gate)	US-PGPU B; USPAT	OR	OFF	2004/09/07 14:30
S82	35	(324/253.ccls. or 327/110. ccls.) and ("and gate" or and-gate)	US-PGPU B; USPAT	OR	OFF	2004/09/07 14:30
S83	7	S82 and (fluxgate or flux adj gate or flux-gate or magnetometer)	US-PGPU B; USPAT	OR	OFF	2004/09/07 15:30

S84	15	(327/176.ccls. or 324/244. ccls. or 324/253.ccls. or 327/172.ccls.) and ((pulse or clock or clk) near3 (control\$5 or generat\$3) same (fluxgate or flux-gate or flux adj gate or magnetometer))	US-PGPU B; USPAT	OR	OFF	2004/09/07 15:43
S85	7	324/253.ccls. and ("and gate" or and-gate)	US-PGPU B; USPAT	OR	OFF	2004/09/07 15:44
S86	81	324/253.ccls. and (gate)	US-PGPU B; USPAT	OR	OFF	2004/09/07 15:54
S87	282	(324/253).CCLS.	US-PGPU B; USPAT	OR	OFF	2004/09/07 15:54
S88	251	S87 and @pd<"20020918"	US-PGPU B; USPAT	OR	OFF	2004/09/07 17:04
S89	1	324/253.ccls. and (converter same complet\$4)	US-PGPU B; USPAT	OR	OFF	2004/09/07 17:05
S90	129	"324".clas. and (converter same complet\$4) same (processor or controller)	US-PGPU B; USPAT	OR	OFF	2004/09/08 07:58
S91	2	S90 and (fluxgate or flux-gate or flux adj gate or magnetometer)	US-PGPU B; USPAT	OR	OFF	2004/09/07 17:06
S92	129	"324".clas. and (converter same complet\$4) same (processor or controller)	US-PGPU B; USPAT	OR	OFF	2004/09/08 07:59
S93	911011	"90" and @pd<"20020918"	US-PGPU B; USPAT	OR	OFF	2004/09/08 08:05
S94	2290	"S90" and @pd<"20020918"	US-PGPU B; USPAT	OR	OFF	2004/09/08 08:05
S95	108	S92 and @pd<"20020918"	US-PGPU B; USPAT	OR	OFF	2004/09/08 08:39
S96	263	(324/253).CCLS.	USPAT	OR	OFF	2004/09/08 08:18
S97	0	(A/D or "a to d" or analog-digital or "analog to digital") with convert\$4 with complet3	US-PGPU B; USPAT	OR	OFF	2004/09/08 08:41
S98	0	(A/D or "a to d" or analog-digital or "analog to digital") with convert\$4 same complet3	US-PGPU B; USPAT	OR	OFF	2004/09/08 08:41

S99	87748	(A/D or "a to d" or analog-digital or "analog to digital")	US-PGPU B; USPAT	OR	OFF	2004/09/08 08:41
S100	2121	(A/D or "a to d" or analog-digital or "analog to digital") with conver\$5 with comple\$3	US-PGPU B; USPAT	OR	OFF	2004/09/08 08:48
S101	1761	S100 and @pd<"20020918"	US-PGPU B; USPAT	OR	OFF	2004/09/08 08:42
S102	6	S101 and (fluxgate or flux-gate or flux adj gate or magnetometer)	US-PGPU B; USPAT	OR	OFF	2004/09/08 08:46
S103	946	S101 and (microprocessor or micro-processor or micro adj processor or controller or microcontroller or micro-controller or micro adj controller)	US-PGPU B; USPAT	OR	OFF	2004/09/08 08:47
S104	389	S103 and sensing	US-PGPU B; USPAT	OR	OFF	2004/09/08 08:48
S105	32	S104 and "324".clas.	US-PGPU B; USPAT	OR	OFF	2004/09/08 08:48
S106	217	(A/D or "a to d" or analog-digital or "analog to digital") with conver\$5 with comple\$3 with (microprocessor or micro-processor or micro adj processor or controller or microcontroller or micro-controller or micro adj controller)	US-PGPU B; USPAT	OR	OFF	2004/09/08 14:33
S107	193	S106 and @pd<"20020918"	US-PGPU B; USPAT	OR	OFF	2004/09/08 08:49
S108	21	S107 and ("327.clas" or "324".clas.)	US-PGPU B; USPAT	OR	OFF	2004/09/08 08:49
S109	2	S106 and (fluxgate or flux-gate or flux adj gate or magnetometer)	US-PGPU B; USPAT	OR	OFF	2004/09/08 08:52

S11 0	267	(A/D or "a to d" or analog-digital or "analog to digital" or ADC) with conver\$5 with complet\$3 with (microprocessor or micro-processor or micro adj processor or controller or microcontroller or micro-controller or micro adj controller)	US-PGPU B; USPAT	OR	OFF	2004/09/08 08:54
S11 1	2	S110 and (fluxgate or flux-gate or flux adj gate or magnetometer)	US-PGPU B; USPAT	OR	OFF	2004/09/08 08:55
S11 2	0	S110 and 324/253.ccls.	US-PGPU B; USPAT	OR	OFF	2004/09/08 08:55
S11 3	1	S110 and 327/110.ccls.	US-PGPU B; USPAT	OR	OFF	2004/09/08 08:55
S11 4	45	(and-gate or "and gate" or "nand gate" or "or gate" or "nor gate") same (generator) same (controller or microcontroller or micro-controller or microprocessor or micro-processor or processor) same (ADC or "analog to digital" or "A/D" or "a to d")	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/08 09:18
S11 5	14	(and-gate or "and gate" or "nand gate" or "or gate" or "nor gate") same (generator) same (controller or microcontroller or micro-controller or microprocessor or micro-processor or processor) with (ADC or "analog to digital" or "A/D" or "a to d")	US-PGPU B; USPAT; USOCR	OR	OFF	2004/09/08 09:20

S11 6	57	(and-gate or "and gate" or "nand gate" or "or gate" or "nor gate") same (generator or pulse adj generator or clk or clock or counter) same (controller or microcontroller or micro-controller or microprocessor or micro-processor or processor) with (ADC or "analog to digital" or "A/D" or "a to d")	US-PGPU B; USPAT; USOCR	OR	ON	2004/09/08 09:21
S11 7	10	S116 and "324".clas.	US-PGPU B; USPAT; USOCR	OR	ON	2004/09/08 10:39
S11 8	34	(microprocessor or micro-processor or processor or microcontroller or micro-controller or controller or pulse adj controller or CPU) with control adj signal with current adj amplifier	US-PGPU B; USPAT; USOCR	OR	ON	2004/09/08 10:40
S11 9	23	S118 and @pd<"20020918"	US-PGPU B; USPAT; USOCR	OR	ON	2004/09/08 10:41
S12 0	4	S119 and "324".clas.	US-PGPU B; USPAT; USOCR	OR	ON	2004/09/08 10:41
S12 1	84342	S119 and "324".clas. or "327".clas.	US-PGPU B; USPAT; USOCR	OR	ON	2004/09/08 10:41
S12 2	7	S119 and ("324".clas. or "327".clas.)	US-PGPU B; USPAT; USOCR	OR	ON	2004/09/08 10:44

S12 3	1	324/253.ccls. and (current adj amplifier and (micro-controller or micro adj controller or microcontroller or controller or processor or microprocessor or micro-processor or micro adj processor) with driv\$3 and (ADC or "analog to digital" or "analog-to-digital" or "a to do" or "A/D"))	US-PGPU B; USPAT; USOCR	OR	ON	2004/09/08 10:55
S12 4	1	324/253.ccls. and (current adj amplifier and (micro-controller or micro adj controller or microcontroller or controller or processor or microprocessor or micro-processor or micro adj processor) with driv\$3 and (ADC or "analog to digital" or "analog-to-digital" or "a to d" or "A/D"))	US-PGPU B; USPAT; USOCR	OR	ON	2004/09/08 11:32
S12 5	1	"6513252".PN.	USPAT; USOCR	OR	OFF	2004/09/08 10:57
S12 6	1	"6084406".PN.	USPAT; USOCR	OR	OFF	2004/09/08 10:58
S12 7	1	"5608320".PN.	USPAT; USOCR	OR	OFF	2004/09/08 10:58
S12 8	1	"5560115".PN.	USPAT; USOCR	OR	OFF	2004/09/08 10:58
S12 9	1	"5170566".PN.	USPAT; USOCR	OR	OFF	2004/09/08 11:04
S13 0	1	"4843865".PN.	USPAT; USOCR	OR	OFF	2004/09/08 11:05
S13 1	1	"4725995".PN.	USPAT; USOCR	OR	OFF	2004/09/08 11:05
S13 2	1	"4503621".PN.	USPAT; USOCR	OR	OFF	2004/09/08 11:06
S13 3	1	"4539760".PN.	USPAT; USOCR	OR	OFF	2004/09/08 11:06
S13 4	1	"5105548".PN.	USPAT; USOCR	OR	OFF	2004/09/08 11:07

S13 5	2	"3387377".PN.	USPAT; USOCR	OR	OFF	2004/09/08 11:07
S13 6	2	"2671275".PN.	USPAT; USOCR	OR	OFF	2004/09/08 11:07
S13 7	1	"5239264".PN.	USPAT; USOCR	OR	OFF	2004/09/08 11:08
S13 8	1	"5039945".PN.	USPAT; USOCR	OR	OFF	2004/09/08 11:08
S13 9	1	"4918824".PN.	USPAT; USOCR	OR	OFF	2004/09/08 11:08
S14 0	1	"4851775".PN.	USPAT; USOCR	OR	OFF	2004/09/08 11:10
S14 1	1	"4733181".PN.	USPAT; USOCR	OR	OFF	2004/09/08 11:10
S14 2	1	"4728888".PN.	USPAT; USOCR	OR	OFF	2004/09/08 13:17
S14 3	1	"4675615".PN.	USPAT; USOCR	OR	OFF	2004/09/08 11:10
S14 4	1	"4403515".PN.	USPAT; USOCR	OR	OFF	2004/09/08 11:10
S14 5	1	"4314200".PN.	USPAT; USOCR	OR	OFF	2004/09/08 11:12
S14 6	1	"4305034".PN.	USPAT; USOCR	OR	OFF	2004/09/08 11:13
S14 7	1	"4241317".PN.	USPAT; USOCR	OR	OFF	2004/09/08 11:14
S14 8	1	"4182987".PN.	USPAT; USOCR	OR	OFF	2004/09/08 11:14
S14 9	1	("3746842").PN.	US-PGPU B; USPAT	OR	OFF	2004/09/08 11:35
S15 0	1	"3637997".PN.	USPAT; USOCR	OR	OFF	2004/09/08 11:34
S15 1	37	"3746842"	US-PGPU B; USPAT	OR	OFF	2004/09/08 11:41

S15 2	78	(fluxgate or magnetometer or flux-gate or flux adj gate) same (counter controller processor microprocessor micro adj processor micro-processor microcontroller micro adj controller micro-controller clk clock) same (A/D "a to d" ADC "analog to digital" "analog-to-digital")	US-PGPU B; USPAT	OR	OFF	2004/09/08 11:45
S15 3	8	(fluxgate or magnetometer or flux-gate or flux adj gate) same (counter controller processor microprocessor micro adj processor micro-processor microcontroller micro adj controller micro-controller clk clock) with amplifier same (A/D "a to d" ADC "analog to digital" "analog-to-digital")	US-PGPU B; USPAT	OR	OFF	2004/09/08 12:46
S15 4	314667	(fluxgate or magnetometer or flux-gate or flux adj gate) same (counter controller processor microprocessor micro adj processor micro-processor microcontroller micro adj controller micro-controller clk clock) with driv\$3 amplifier	US-PGPU B; USPAT	OR	OFF	2004/09/08 12:46
S15 5	137	(fluxgate or magnetometer or flux-gate or flux adj gate) same (counter controller processor microprocessor micro adj processor micro-processor microcontroller micro adj controller micro-controller clk clock) with driv\$3 withamplifier	US-PGPU B; USPAT	OR	OFF	2004/09/08 12:46

S15 6	16	(fluxgate or magnetometer or flux-gate or flux adj gate) same (counter controller processor microprocessor micro adj processor micro-processor microcontroller micro adj controller micro-controller clk clock) with driv\$3 with amplifier	US-PGPU B; USPAT	OR	OFF	2004/09/08 14:23
S15 7	1	"5939881".PN.	USPAT; USOCR	OR	OFF	2004/09/08 12:49
S15 8	1	"4677381".PN.	USPAT; USOCR	OR	OFF	2004/09/08 12:50
S15 9	1	"5329269".PN.	USPAT; USOCR	OR	OFF	2004/09/08 12:50
S16 0	1	"5014006".PN.	USPAT; USOCR	OR	OFF	2004/09/08 12:51
S16 1	1	"5008621".PN.	USPAT; USOCR	OR	OFF	2004/09/08 12:51
S16 2	1	"4502010".PN.	USPAT; USOCR	OR	OFF	2004/09/08 12:52
S16 3	1	"3683668".PN.	USPAT; USOCR	OR	OFF	2004/09/08 12:59
S16 4	1	"3899834".PN.	USPAT; USOCR	OR	OFF	2004/09/08 12:59
S16 5	1	"4698912".PN.	USPAT; USOCR	OR	OFF	2004/09/08 13:00
S16 6	1	("5333110").PN.	US-PGPU B; USPAT	OR	OFF	2004/09/08 13:17
S16 7	1	(fluxgate or magnetometer or flux-gate or flux adj gate) same (counter controller processor microprocessor micro adj processor micro-processor microcontroller micro adj controller micro-controller clk clock) with driv\$3 with amplifier	EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2004/09/08 14:25
S16 8	174075	LEE, WOO-JONG.in.	EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2004/09/08 14:25

S16 9	10	WOO-JONG.in.	EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2004/09/08 14:29
S17 0	21	SANG-ON.in.	EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2004/09/08 14:32
S17 1	1	SEUNG-CHOUL.in.	EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2004/09/08 14:32
S17 2	19	(A/D or "a to d" or analog-digital or "analog to digital" or ADC) with conver\$5 with complet\$3 with (microprocessor or micro-processor or micro adj processor or controller or microcontroller or micro-controller or micro adj controller)	EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2004/09/08 14:34
S17 3	136	((fluxgate or flux-gate or flux adj gate or magnetometer or MR) same converter same controller)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2004/09/08 14:37
S17 4	21	((fluxgate or flux-gate or flux adj gate or magnetometer) same converter same controller)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2004/09/08 15:00
S17 5	251	324/253.ccls. and @pd<"20020918"	US-PGPU B; USPAT	OR	OFF	2004/09/09 09:55
S17 6	1	("4855965").PN.	US-PGPU B; USPAT	OR	OFF	2004/09/09 09:56

S17 7	50	("4855965" "5764055" "6028427" "4384254" "4447776" "4590679" "4780858" "4929899" "5272619" "5317251" "5689445" "5432445" "5608320" "5870328" "5951881" "4864238" "4967156" "5014006" "5467083" "3571700" "3638074" "3800213" "4037328" "4277751" "4293815" "4346605" "4349781" "4380716" "4439732" "4475078" "4536710" "4539522" "4546550" "4590425" "4623842" "4626781" "4766577" "4769599" "4780862" "4845434" "4868792" "4879694" "4891587" "4894615" "4977374" "5008612" "5199178" "5239290" "5254987" "5258755").pn.	US-PGPU B; USPAT	OR	OFF	2004/09/09 14:05
S17 8	1	"5287295".PN.	USPAT; USOCR	OR	OFF	2004/09/09 10:19
S17 9	1	"5161311".PN.	USPAT; USOCR	OR	OFF	2004/09/09 10:19
S18 0	1	"4953305".PN.	USPAT; USOCR	OR	OFF	2004/09/09 10:19
S18 1	1	"4953305".PN.	USPAT; USOCR	OR	OFF	2004/09/09 10:19
S18 2	1	"4677381".PN.	USPAT; USOCR	OR	OFF	2004/09/09 10:19
S18 3	1	"4546551".PN.	USPAT; USOCR	OR	OFF	2004/09/09 10:19
S18 4	1	"3942258".PN.	USPAT; USOCR	OR	OFF	2004/09/09 10:20
S18 5	2	"6282803"	US-PGPU B; USPAT	OR	OFF	2004/09/09 14:50
S18 6	4	"5333110"	US-PGPU B; USPAT	OR	OFF	2004/09/09 14:05
S18 7	1	"4807462".PN.	USPAT; USOCR	OR	OFF	2004/09/09 14:07

S18 8	1	"4831563".PN.	USPAT; USOCR	OR	OFF	2004/09/09 14:07
S18 9	1	"3899834".PN.	USPAT; USOCR	OR	OFF	2004/09/09 14:07
S19 0	1	"3991361".PN.	USPAT; USOCR	OR	OFF	2004/09/09 14:07
S19 1	1	"4414753".PN.	USPAT; USOCR	OR	OFF	2004/09/09 14:07
S19 2	1	"4424631".PN.	USPAT; USOCR	OR	OFF	2004/09/09 14:07
S19 3	1	"4425717".PN.	USPAT; USOCR	OR	OFF	2004/09/09 14:08
S19 4	1	"4505054".PN.	USPAT; USOCR	OR	OFF	2004/09/09 14:08
S19 5	1	"4546551".PN.	USPAT; USOCR	OR	OFF	2004/09/09 14:08
S19 6	1	"4622646".PN.	USPAT; USOCR	OR	OFF	2004/09/09 14:08
S19 7	1	"4660161".PN.	USPAT; USOCR	OR	OFF	2004/09/09 14:08
S19 8	1	"4672565".PN.	USPAT; USOCR	OR	OFF	2004/09/09 14:08
S19 9	1	"4677381".PN.	USPAT; USOCR	OR	OFF	2004/09/09 14:09
S20 0	1	"4698912".PN.	USPAT; USOCR	OR	OFF	2004/09/09 14:09
S20 1	1	"4797841".PN.	USPAT; USOCR	OR	OFF	2004/09/09 14:09
S20 2	1	"5046031".PN.	USPAT; USOCR	OR	OFF	2004/09/09 14:09
S20 3	1	"3899834".PN.	USPAT; USOCR	OR	OFF	2004/09/09 14:11
S20 4	1	"3991361".PN.	USPAT; USOCR	OR	OFF	2004/09/09 14:11
S20 5	1	"4424631".PN.	USPAT; USOCR	OR	OFF	2004/09/09 14:11
S20 6	1	"5021962".PN.	USPAT; USOCR	OR	OFF	2004/09/09 14:11
S20 7	1	"4873655".PN.	USPAT; USOCR	OR	OFF	2004/09/09 14:11

S20 8	1	"4866627".PN.	USPAT; USOCR	OR	OFF	2004/09/09 14:11
S20 9	1	"4694583".PN.	USPAT; USOCR	OR	OFF	2004/09/09 14:11
S21 0	1	"4622843".PN.	USPAT; USOCR	OR	OFF	2004/09/09 14:12
S21 1	0	"10658380"	US-PGPU B; USPAT	OR	OFF	2004/09/09 14:51
S21 2	0	US20040051566A1	US-PGPU B; USPAT	OR	OFF	2004/09/09 14:51
S21 3	1	"20040051566"	US-PGPU B; USPAT	OR	OFF	2004/09/09 15:11
S21 4	4	"5744956"	US-PGPU B; USPAT	OR	OFF	2004/09/09 16:43
S21 5	33	"4622843"	US-PGPU B; USPAT	OR	OFF	2004/09/09 16:43